

## CLAIMS

1. A ceramic substrate provided with a conductor layer on the surface of said ceramic substrate or inside said ceramic substrate,  
5 wherein: the ratio ( $t_2/t_1$ ) of the average thickness of said conductor layer ( $t_2$ ) to the average thickness of said ceramic substrate ( $t_1$ ) is less than 0.1; and a dispersion of the thickness of the conductor layer to the  
10 average thickness of the conductor layer is in a range of - 70 to +150%.
2. The ceramic substrate according to claim 1,  
15 wherein said ceramic substrate is in a disc-shape with a diameter exceeding 150 mm.
3. The ceramic substrate according to claim 1 or 2,  
20 wherein the thickness of said ceramic substrate is 25 mm or less.
4. The ceramic substrate according to any of claims 1 to 3,  
25 wherein said conductor layer is an electrostatic electrode.
5. The ceramic substrate according to any of claims 1 to 3,  
30 wherein said conductor layer is a resistance heating element.
6. The ceramic substrate according to any of claims 1 to 3,  
35 wherein said conductor layer is any of a chuck top electrode, a guard electrode and a ground electrode.